

CURRICULUM VITAE

1. PERSONAL DETAILS

Name : Mr. Bhupendra Das
Father's Name : Shri Jagmohan Das
Date of Birth : 1st June 1993
Gender : Male
Nationality : Indian
Category : OBC
Marital Status : Single
Local Address : c/o Harishanker Gupta, 55/11 Nehru
Nagar (East), Bhilai, Durg,
Chhattisgarh - 490020

Permanent Address : H. N. 51, Village – Chitabahar,
P.O. - Bhitthi Kalan, Ambikapur,
Surguja, Chhattisgarh – 497001

Contact Number : 7879586493
Email id : bhupendradas1693@hotmail.com
bhupendravtytdurg@gmail.com

2. EMPLOYMENT STATUS

Designation : Assistant Professor(Self Finance Scheme)
Department : Electronics
Address : Department of Physics
Govt. V. Y. T. PG Autonomous College
Durg, Chhattisgarh

3. ACADEMIC QUALIFICATION

COURSE	INSTITUTION	BOARD/ UNIVERSITY	YEAR of COMPLETION	AGGREGATE (%)
M. TECH. (Instrumentation & Control)	BHILAI INSTITUTE OF TECHNOLOGY, DURG (C.G.)	CHHATTISGARH SWAMI VIVEKANAND TECHNICAL UNIVERSITY, BHILAI	2017	78.43%
B.E. (Electronics & Telecommunication)	SHRI SHANKARACHARYA ENGINEERING COLLEGE, BHILAI (C.G.)	CHHATTISGARH SWAMI VIVEKANAND TECHNICAL UNIVERSITY, BHILAI	2014	76.74%
HSC	SARASWATI H S SCHOOL, AMBIKAPUR	CHHATTISGARH BOARD OF SECONDARY EDUCATION	2010	88.80%
SSC	SARASWATI H S SCHOOL, AMBIKAPUR	CHHATTISGARH BOARD OF SECONDARY EDUCATION	2008	86.17%

4. OTHER QUALIFICATION

UGC-National Eligibility Test (NET)	:	July 2018
Graduate Aptitude Test in Engineering (GATE)	:	March 2014
		March 2015
		March 2016
		March 2017
		March 2018
		March 2019

5. EXPERIENCES

Name of the Post	Name of Institute	Duration
Electronics Lab Demonstrator	Govt. V.Y.T. PG Autonomous College, Durg	From September 2018 to April 2019
Guest Faculty (Electronics)	Govt. V.Y.T. PG Autonomous College, Durg	<ul style="list-style-type: none">From August 2019 to February 2020From October 2020 to till date
Total Experience		2 Year 2 Months

6. INDUSTRIAL/VOCATIONAL TRAINING

Class	Name of Institute	Duration
B.E. 4 th Semester	Doordarshan Kendra, Raipur	4 Weeks
B.E. 6 th Semester	C.S.P.T.C.L. Bhilai	4 Weeks
B.E. 7 th Semester	Netratva Technology, Bhilai	4 Weeks
M. Tech 2 nd Semester	ACC Limited Jamul, Durg	4 Weeks
Total Training		4 Months

7. PROJECT/RESEARCH

Class	Project/Thesis Title
B.E. (Electronics & Telecommunication)	SMS Based Digital Notice Board
M. Tech (Instrumentation & Control)	Robust LMI Parser Based Filter Design for Linear Discrete-Time Delayed Systems

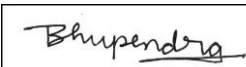
8. PUBLICATIONS

- Bhupendra Das, "Linear Uncertain Discrete Time Delay System: A Survey", International Journal of Innovative Research and Advanced Studies (IJIRAS), Volume 4, issue 6, June 2017.

- Bhupendra Das, Siva Kumar Tadepalli, Suchitra Pandey, “Stability Analysis of Uncertain and Delayed Discrete Systems Using ROLMIP” 2018 International conference on Advances in Communication and Computing Technology (ICACCT), Sangamner, 2018, pp. 35-39.
- Suchitra Pandey, Bhupendra Das, Siva Kumar Tadepalli, “Comments on ‘New finite-sum inequalities with applications to stability of discrete time-delay systems’” Automatica, Volume 91, pp. 320-321, May 2018.

9. WORKSHOP/SEMINAR/TRAINING PROGRAMMES ATTENDED

CATEGORY	NATIONAL
Workshop	04
Seminar	01
FTP/FDP	03
Total	08



Place: Durg

Signature