

CURRICULUM VITA

1. PERSONAL DETAILS

NAME : Dr. (Mrs.) Swagota Bera
DATE OF BIRTH : 3rd August, 1976
SEX : Female
NATIONALITY : Indian
MARITAL STATUS : Married
RESIDENTIAL ADDRESS : C/O. Mr. A. K. Bera
Qtr. No. 3 / C, Street - 3,
Sector – 8, Bhilai (C.G), 490006
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2. DESIGNATION

Asst. Professor (Self-Finance Scheme)

3. EDUCATIONAL QUALIFICATION

EXAMINATION	BOARD/ UNIVER SITY	YEAR	MARKS (Percentage and Grade)	INSTITUTION
Ph.D. (ETC)	CSVTU, Bhilai	2019	Awarded on 20 th December 2019	Shri Shankaracharya Technical Campus, Bhilai
ME (Specialization in Communication)	CSVTU, Bhilai	2008	CPI = 9.1 82.86 % (College Topper)	Shri Shankaracharya College of Engineering &Technology, Bhilai
M.Sc. (Electronics) (B.E. Equivalent as per The Gazette of India No. 249] NEW DELHI, FRIDAY, JUNE 10, 2016/JYAISTHA 20, 1938)	Pt. R.S.S.U, Raipur	1999	76.9% (1 st position in University)	S.O.S in Electronics Pt. Ravishankar University, Raipur.
B.Sc. (PCM)	Pt. R.S.S.U, Raipur	1997	75% (6 th position in University)	Govt. N.C.Jain College, Dalli- Rajhara.
HSC	CBSE	1994	78.9%	Senior Secondary School, Dalli- Rajhara.
SSC	CBSE	1992	70.0 %	Senior Secondary School, Dalli Rajhara

** Certification Course on “Machine Learning, Data Science and Deep Learning with Python”
on 1st Sept 2020 by Udemy.

** Certification Course on “**Tableau 2020 A-Z: Hands-On Tableau Training for Data Science**” on 14th July 2021 by Udemy.

EXPERIENCES

Sl. No.	Name of the Post	Institute	Duration
1.	Assistant Professor (Lecturer)	Shri Shankaracharya College of Engineering and Technology, Bhilai (SSCET)	From 5 th August 2000 to 17 th August 2009.
2.	Assistant Professor (Lecturer)	Dev Sanskriti College of Engineering and Technology, Durg (DSCET)	From 18 th August 2009 to 27 th January 2010.
3	Associate professor (Reader)	Dev Sanskriti College of Engineering and Technology, Durg (DSCET)	From 28 th January 2010 to 31 st August 2012.
4.	Associate professor (Reader)	Shri Shankaracharya Institute of Engineering and Technology, Durg (SSIET)	From 1 st September 2010 to 30 th November 2016.
5.	Guest Faculty	Electronics/M.Tech in Opto Electronics and Laser Technology, Pt. Ravishankar Shukla University, Raipur	From February to March 2017.
6.	Guest Faculty	Electronics Department Govt. V.Y.T.P.G. Autonomous College, Durg	<ul style="list-style-type: none"> • From August 2017 to February 2018. • From August 2018 to February 2019. • From August 2019 to February 2020. • From November 2020 to till date
Total Experience			<p>*Engineering College— 16 Year 3 Months</p> <p>*University/PG College – 2 Year</p>

4. MAJOR ACHIEVEMENTS

- Reviewer of one of the SCI journal, **Security and Communication Network**, Willey publication.
- Copyright on “**A DWT Feature Based Blind Steganalysis in Transform Domain**”.
- Copyright on “**A Gabor Filter Based Blind Steganalysis for Transform Domain**”.
- Patent on “**Fitness Intelligent Predictive Analytics using Deep Learning**”.
- Delivered invited talk on “**Novel Materials and Medical Imaging Phosphors**” at Govt. V.Y.T.P.G Autonomous College, Bhilai (29th and 30th Jan 2020).
- Research paper citations are more than 50.

5. MEMBERSHIP OF PROFESSIONAL/ACADEMIC BODIES

- Associates of “**Institution of Engineers**” having registration No.A-5489971 from Nov 2009. Upgraded to Associate Member having registration No.AM138779 -3.
- Member of “**Institute of Electrical and Electronics Engineering**” having registration No.-92227495.
- Life member of “**Indian Society for Technical Education**” having Registration No.-LM-40808.

6. PUBLICATIONS:

PUBLICATION	NATIONAL	INTERNATIONAL	TOTAL
Book	-	02	02
Journals	04	16	20
Conferences	16	07	23
Total	19	25	45

7. PARTICIPATION AND CONTRIBUTION IN THE AREA OF ACADEMIC AND PROFESSIONAL EXPERTISE

- Delivered invited talk on “**Novel Materials and Medical Imaging Phosphors**” at Govt. V.Y.T.P.G Autonomous College, Bhilai (29th and 30th Jan 2020).

8. RESEARCH

Ph.D. Thesis Topic	An efficient Feature Based Blind Steganalysis in Transform Domain
M.E Thesis Topic	Analysis of real time Stego images using MATLAB for the detection of illegal transaction
Projects Guided	<ol style="list-style-type: none">Lossless and Lossy Image Compression of real time images, Nitesh More, M.E (CSE), RCET, Bhilai (2011).Hardware implementation of Steganography Techniques, Umashankar Dewangan, M.E (E&Tc), SSCET, Bhilai (2011).Optimization of the Steganography Techniques, Parmanand Dewangan, M.E (E&Tc), SSCET, Bhilai (2011).

9. INDUSTRIAL TRAINING

- Undergone training in Electronic Instrumentation section in Universal Cables Limited, Satna, 15 July, 1998.
- Undergone training in Bhilai Technical Institute, BSP, Bhilai, 28th May to 7th July, 2007.

10. CO-ORDINATOR

- Worked for ISO certification in 2002 at SSCET.
- Worked as a girl’s hostel warden from 2000 to 2003 of SSCET, Bhilai.
- Worked as a coordinator in the filling for NBA certification in 2004 at SSCET.
- Worked in the SSIET Akhand Jyot project, supported by the DEO, Durg, Chhattisgarh, a computer training project for C.G. Govt. School, especially where computer facilities are not proper on 15th December 2015 in SSIET, Durg.
- Worked as a central co-ordinator of IIT spoken tutorial project, IIT Bombay of the college from 2013 to 2015 in SSIET. (Conducted almost 40 training program of FOSS and 12 test related to the IIT certification course).
- Worked as the anti-ragging committee member.
- Worked as a coordinator for Women’s Grievance Redressal Cell from 2013 to 2016 in SSIET, Durg.
- Worked as R&D coordinator in the college and associates with R&D cell in CSVTU, Bhilai in 2014.
- Worked as convener for the Three-Days National Level Workshop on “**Cyber Forensic and Information Security**” from 22nd to 23rd March 2014 in SSIET, Durg.
- Organizing committee member for One-Day National Seminar on “**3D Printing and Additive Manufacturing Technologies**” on 17th January 2015.
- Worked as coordinator for the Two-Days Workshop on “**Embedded System**” from 15th to 16th February 2017 in PRSU, Raipur.
- Worked as a technical session coordinator for the National Workshop “**NSSEMA-2017**” from 28th to 30th March 2017 in PRSU, Raipur.

- Worked as coordinator for the Three-Days National Level Workshop and Training on “**Arduino System**” from 21st to 23rd September 2017 in PRSU, Raipur.
- Worked as co-ordinator for the Three-Days National Level on “**IoT (Internet of Things) using Node MCU**” from 26th to 28th March 2018 in PRSU, Raipur.
- Worked as convener for the Three-Days State Level Workshop on “**Robotics and Embedded System**”, 2018.

11. EXAMINAR

- Paper setter & Evaluator for the CSVTU, Bhilai.
- As external for M.E. projects in Electronics and Telecommunications.

12. AWARDS & FELICITATIONS

- Gold Medals & certificates by Pt. Ravishankar Shukla University, Raipur for securing 1st place in the order of merit M.Sc. (Electronic Science) in university in 1999.
- National Scholarship by Pt. Ravishankar Shukla University, Raipur for securing 6th place in the order of merit B.Sc. (PCM) in university in 1997.
- Scholarship by Bhilai Steel Plant, Bhilai for securing merit position in class 12th Central Board Exam, 1994.

13. WORKSHOP/SEMINAR/TRAINING PROGRAMMES ATTENDED

CATEGORY	NATIONAL
Workshops	12
Seminars	02
FTP/FDP/STP	07
Total	21

MY STRENGTH:

My strength is my determination, dedication and honesty to the work. I give my best to my work with responsibility and commitment.

MY WEAKNESS:

My greatest weakness is that I sometimes find difficult to say no to request. Though I am capable to judge right or wrong and do the required action. I felt very anxiety when I am not able to finish any allotted work within deadline which makes me unhealthy.

REFERENCES:

1. **Dr.(Mrs.) Kavita Thakur**
Professor
S.O.S. in Electronics and Photonics
Raipur
2. **Dr.(Mrs.) Himani Agrawal**
Head and Professor
Department of Electronics and Telecommunication Engineering
Govt. Polytechnique College
Durg
3. **Dr. Bikesh Kumar Singh**
Professor
Department of Bio-Medical Engineering
National Institute of Technology

Raipur

Place: Bhilai

Date: 03th July 2021



Signature